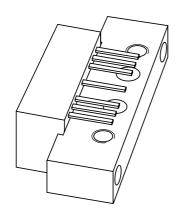
DISCRETE SEMICONDUCTORS

DATA SHEET



BGY1085A 1000 MHz, 18.5 dB gain push-pull amplifier

Product specification Supersedes data of 1997 Apr 15 2001 Oct 25





Philips Semiconductors

1000 MHz, 18.5 dB gain push-pull amplifier

BGY1085A

FEATURES

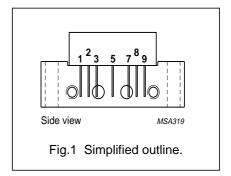
- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Gold metallization ensures excellent reliability.

DESCRIPTION

Hybrid high amplifier module for CATV systems operating over a frequency range of 40 to 1000 MHz at a supply voltage of +24 V (DC).

PINNING - SOT115J

PIN	DESCRIPTION		
1	input		
2	common		
3	common		
5	+V _B		
7	common		
8	common		
9	output		



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Gp	power gain	f = 50 MHz	18	19	dB
		f = 1000 MHz	18.5	1	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	_	240	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
Vi	RF input voltage	_	65	dBmV
T _{stg}	storage temperature	-40	+100	°C
T _{mb}	operating mounting base temperature	-20	+100	°C

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CHARACTERISTICS

Table 1 Bandwidth 40 to 1000 MHz; $T_{case} = 30$ °C; $Z_S = Z_L = 75 \Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Gp	power gain	f = 50 MHz	18	_	19	dB
		f = 1000 MHz	18.5	_	_	dB
SL	slope cable equivalent	f = 40 to 1000 MHz	0	_	2	dB
FL	flatness of frequency response	f = 40 to 1000 MHz	_	_	±0.3	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	_	_	dB
		f = 80 to 160 MHz	18.5	_	_	dB
		f = 160 to 320 MHz	17	_	_	dB
		f = 320 to 640 MHz	15.5	_	_	dB
		f = 640 to 1000 MHz	14	_	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	20	_	_	dB
		f = 80 to 160 MHz	18.5	_	_	dB
		f = 160 to 320 MHz	17	_	_	dB
		f = 320 to 640 MHz	15.5	_	_	dB
		f = 640 to 1000 MHz	14	_	_	dB
CTB composite triple beat	composite triple beat	85 channels flat; V _o = 44 dBmV; measured at 595.25 MHz	-	_	-58	dB
		110 channels flat; V _o = 44 dBmV; measured at 745.25 MHz	-	_	-53	dB
		150 channels flat; V _o = 40 dBmV; measured at 985.25 MHz	-	-53	_	dB
X _{mod}	cross modulation	85 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	-	_	-58	dB
		110 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	-	_	-54	dB
		150 channels flat; V _o = 40 dBmV; measured at 55.25 MHz	-	-54	_	dB
	composite second order distortion	85 channels flat; V _o = 44 dBmV; measured at 596.5 MHz	-	_	-60	dB
		110 channels flat; V _o = 44 dBmV; measured at 746.5 MHz	-	_	-56	dB
		150 channels flat; V _o = 40 dBmV; measured at 986.5 MHz	-	-56	_	dB

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
d ₂	second order distortion	note 1	_	_	-72	dB
		note 2	_	_	-65	dB
		note 3	_	-68	_	dB
Vo	output voltage	d _{im} = -60 dB				
		note 4	61	_	_	dBmV
		note 5	60	_	_	dBmV
		note 6	57	_	_	dBmV
F	noise figure	f = 50 MHz	_	_	5.5	dB
		f = 550 MHz	_	_	6	dB
		f = 600 MHz	_	_	6	dB
		f = 650 MHz	_	_	6.5	dB
		f = 750 MHz	_	_	7	dB
		f = 860 MHz	_	_	7.5	dB
		f = 1000 MHz	_	_	7.5	dB
I _{tot}	total current consumption (DC)	note 7	_	_	240	mA

Notes

- 1. $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$ $f_q = 541.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at $f_p + f_q = 596.5 \text{ MHz}.$
- 2. $f_p = 55.25$ MHz; $V_p = 44$ dBmV; $f_q = 691.25$ MHz; $V_q = 44$ dBmV; measured at $f_p + f_q = 746.5$ MHz.
- $\begin{array}{ll} 3. & f_p = 55.25 \text{ MHz; V}_p = 40 \text{ dBmV;} \\ f_q = 931.25 \text{ MHz; V}_q = 40 \text{ dBmV;} \\ \text{measured at } f_p + f_q = 986.5 \text{ MHz.} \end{array}$
- 4. $f_p = 590.25$ MHz; $V_p = V_o$; $f_q = 597.25$ MHz; $V_q = V_o 6$ dB; $f_r = 599.25$ MHz; $V_r = V_o 6$ dB; measured at $f_p + f_q f_r = 588.25$ MHz.
- 5. $f_p = 740.25 \text{ MHz}; V_p = V_o;$ $f_q = 747.25 \text{ MHz}; V_q = V_o -6 \text{ dB};$ $f_r = 749.25 \text{ MHz}; V_r = V_o -6 \text{ dB};$ measured at $f_p + f_q - f_r = 738.25 \text{ MHz}.$
- $\begin{aligned} &6. & f_p = 980.25 \text{ MHz; } V_p = V_o; \\ & f_q = 987.25 \text{ MHz; } V_q = V_o 6 \text{ dB;} \\ & f_r = 989.25 \text{ MHz; } V_r = V_o 6 \text{ dB;} \\ & \text{measured at } f_p + f_q f_r = 978.25 \text{ MHz.} \end{aligned}$
- 7. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.

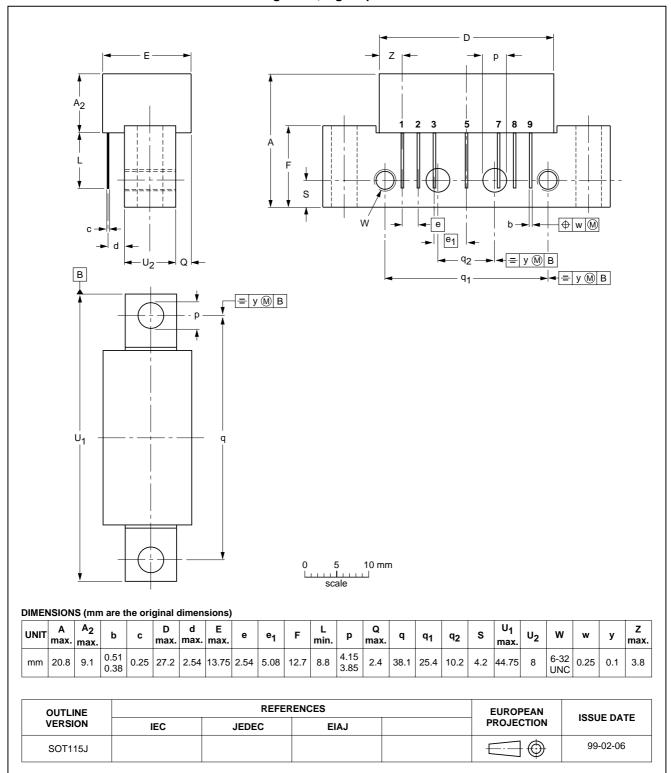
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PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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NOTES

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Printed in The Netherlands

613518/04/pp8

Date of release: 2001 Oct 25

Document order number: 9397 750 08824

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